



2815

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Izuo Iida  
Serial No. : 09/876,554  
Filed : June 7, 2001  
Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

Art Unit : 2815  
Examiner : N. Richards

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	10	-	10	=	0	\$0
Independent	5	-	4	=	1	\$84
First Presentation of Multiple Dependent Claims						\$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 0 month extension of time.

\$0

TOTAL FEE DUE

\$84

A check for \$84 is attached.

Respectfully submitted,

Date: \_\_\_\_\_

5/5/03

Paul A. Levy  
Reg. No. 45,748

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30146167.doc

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## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 5, 2003

Date of Deposit

Signature

Gina Maldonado

Typed or Printed Name of Person Signing Certificate